

FIGURE 2

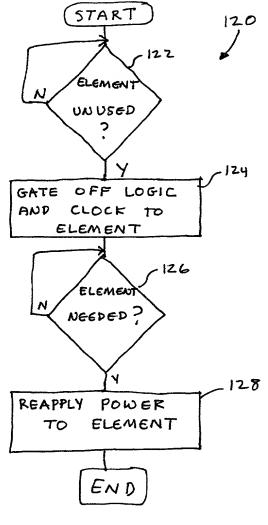


FIGURE 3

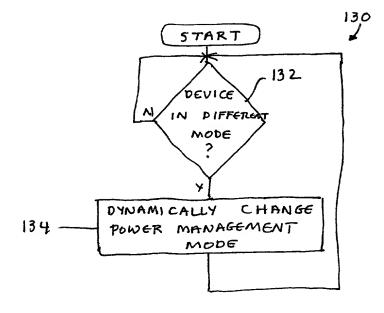
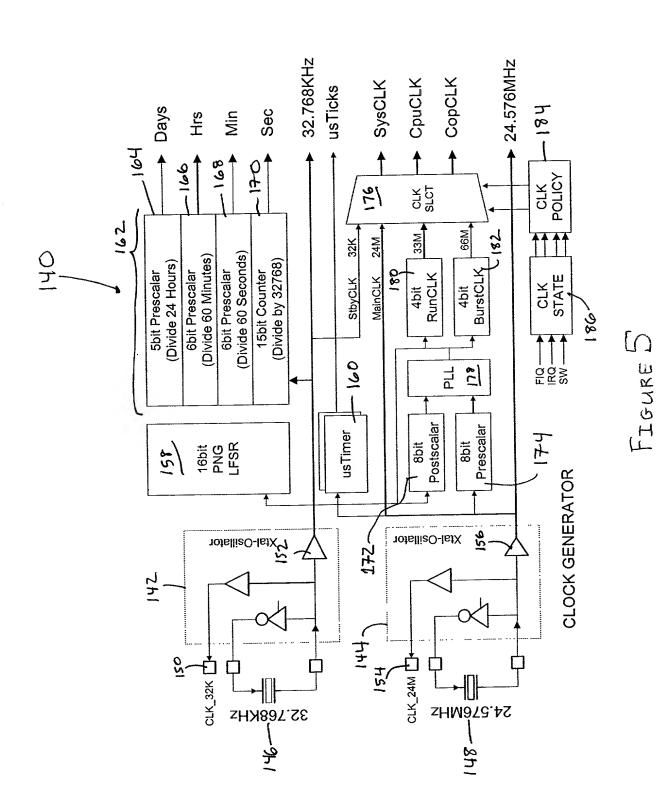


FIGURE 4



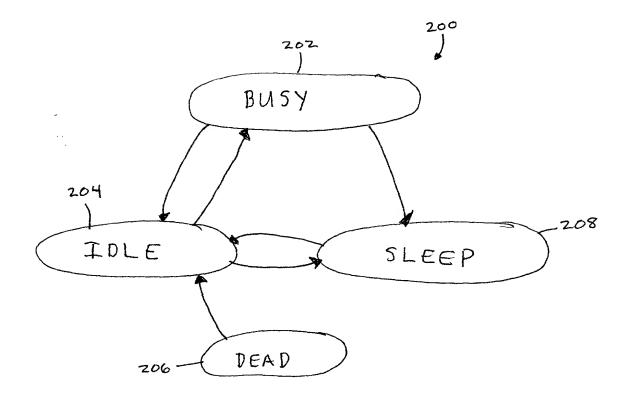


FIGURE 6

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## LEPROOTE POWER MANAGEMENT REGISTER DESCRIPTIONS

PP6001B POWER MANAGEMENT REGISTER DESCRIPTIONS																	
bit15	bit14	bit13	bit12	bit11	bit10	bit9	bit8	bit7	bit6	bit5	bit4	bıt3	bit2	bit1	bit0		
MSTK	MMC	SMC	CF	LCD.E	LCD.C	UART	12C	125	SPDIF	AC97	TMR	RTC	C.EXT	C.24M	C.32K	CF00:500Q	222
24MHz	24MHz	24MHz	sysclk	sysclk	24MHz	24MHz	24MHz	24MHz	24MHz	sysclk	24MHz	.32KHz	SYS <i>C</i> LK	24MHz	32KHz	CF00:5000	222
rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	CCH1	CCHO	rsvd	IDE	USB	GPIO	CF00:5004	-224
rsvd	rsvd.	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	sysc1k	24/48	sysclk	CF00:5004	-224
AUTO	rsvd	rsvd	rsvd	COF. STATE				AUTO	rsvd	rsvd	rsvd	Ç.	PU.STAT	E		CF00:5008	230
COP.	.FIQ	COP	. IRÇ	SOP.2	KAIN	COP.	IDLE	C,bn	FIQ	CPU.	IRQ	CPU.	MAIN	CPU	.IDLE	CF00:500C	34 .
PLL	X24N	X24N X32K 0			SAS CTK SOMYCE				BURST_CLK_RATE				RUN_CLK_RATE				-234
rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	CF00:5014	7226
rsvd	rsvd	rsvd	rsvd	rsvđ	rsvd	rsvd	rsvd	PLL_PRESCALAR_VALUE							CF00:5018	7.22	
rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	PLL_POSTSCALAR_VALUE							CF00:501C	7	
	FREE RUNNING_PLL_COUNTER (PNG_LFSR)															CF00:5020	
0	0	z.swr	Z.WDT	s.swr	s.WDT	P.SWR	P.WDT	0	٥	WDT.E	WDT.X	. 0	V.SYS	W.COP	W.CPU	CF00:502C	
MSTK	MMC	SMC	CF	LCD.B	LCD.C	UART	12C	125	SPDIF	AC97	TMR	0	SYS.R	COP.R	CPU.R	CF00:5030	
rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	rsvd	ravd	rsvd	SCH1	CCEO	rsvd	IDE	USB	GPIO	CF00:5034	